

● PRINTER RUSH ●

(PTO ASSISTANCE)

Application : <u>02/a92 397</u>	Examiner : <u>Vartanian, H</u>	GAU : <u>2634</u>
From: <u>BYC</u>	Location: <u>IDC</u> FMF FDC	Date: <u>4/27/05</u>
Tracking #: <u>06086264</u>		Week Date: <u>3/14/05</u>

9942

DOC CODE	DOC DATE	MISCELLANEOUS
<input type="checkbox"/> 1449	_____	<input type="checkbox"/> Continuing Data
<input type="checkbox"/> IDS	_____	<input type="checkbox"/> Foreign Priority
<input checked="" type="checkbox"/> <u>CLM</u>	<u>6/29/2001</u>	<input type="checkbox"/> Document Legibility
<input type="checkbox"/> IIFW	_____	<input type="checkbox"/> Fees
<input type="checkbox"/> SRFW	_____	<input type="checkbox"/> Other
<input type="checkbox"/> DRW	_____	
<input type="checkbox"/> OATH	_____	
<input type="checkbox"/> 312	_____	
<input type="checkbox"/> SPEC	_____	

[RUSH] MESSAGE: Original claim 3 does not end with a period.
Please Respo we.

Thank you
BYC

[XRUSH] RESPONSE: _____

John

INITIALS [Signature]

for generating a frequency and phase corrected output signal in response to said digital receiver receiving said updated estimated frequency error estimate and said updated estimated phase error estimate .

4. The frequency and phase synchronizer system of claim 1 wherein said unknown frequency offset value is determined by:

generating a first product by multiplying said first sequence of even numbered samples by a first parameter;

generating a first complex exponential value by applying a first discrete time voltage controlled oscillator to said frequency error estimate;

generating a second product by multiplying said first product and said first complex exponential value;

generating a third product by multiplying said second sequence of odd numbered samples by a second parameter;

generating a second complex exponential value by applying a second discrete time voltage controlled oscillator to said frequency error estimate;

generating a fourth product by multiplying said third product and said second complex exponential value;

generating a sequence of first sum signals SUM_l by adding said second and fourth products, where l is an index and $1 \leq l \leq N$ and N is a positive integer ;